

Description

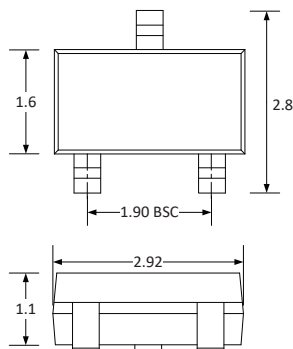
RClamp® TVS diodes are designed to protect sensitive electronics from damage or latch-up due to ESD, and EOS. They feature large cross-sectional area junctions for conducting high transient currents. They offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

RClamp04022S is in a SOT23-3 Lead package. The Leads are finished with lead-free matte tin. They may be used to meet the ESD immunity requirements of IEC 61000-4-2 ($\pm 30\text{kV}$ air, $\pm 30\text{kV}$ contact discharge). Low maximum capacitance (0.75pF at $V_R=0\text{V}$) minimizes loading on sensitive circuits. Each device will protect two high speed data lines operating at 4 volts.

Applications

- USB 2.0
- LVDS
- GPIO
- Analog Video
- USB Type-C
- Keypad

Nominal Dimensions



Nominal Dimensions in mm

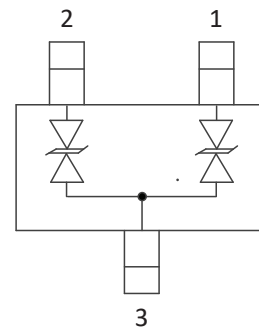
Features

- High ESD withstand voltage
 - IEC 61000-4-2 (ESD): $\pm 30\text{kV}$ (contact), $\pm 30\text{kV}$ (air)
 - IEC 61000-4-5 (Lightning): 18A ($8/20\mu\text{s}$)
 - IEC 61000-4-4 (EFT): 4kV , 80A
- Low capacitance: 0.75pF Max
- Protects two high speed data lines
- Working voltage: 4V
- Low ESD clamping voltage
- Low dynamic resistance: 0.12Ω typical
- Low reverse leakage current
- Solid-state silicon-avalanche technology

Mechanical Characteristics

- Package: SOT23 3L
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Molding compound flammability rating: UL 94V-0
- Lead Finish: Pb-Free
- Marking: Marking code + Date Code
- Packaging: Tape and Reel

Schematic and Pin Configuration



SOT23 3L

Absolute Maximum Rating

RATING	SYMBOL	VALUE	UNITS
Peak Pulse Power (tp = 8/20μs)	P_{PK}	100	W
Peak Pulse Current (tp = 8/20μs)	I_{PP}	18	A
ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V_{ESD}	±30	kV
ESD per IEC 61000-4-2 (Air) ⁽¹⁾		±30	
Operating Temperature	T_J	-40 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics

T=25°C unless otherwise specified

All measurements from Pin 1 to Pin 3 or Pin 2 to Pin 3

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage	V_{RWM}				4	V
Reverse Breakdown Voltage	V_{BR}	$I_t = 1mA$	6	7.6	10	V
Reverse Leakage Current	I_R	$V_{RWM} = 4V$		<10	50	nA
Clamping Voltage ⁽²⁾	V_C	$t_p = 1.2/50\mu s$ (Voltage), 8/20us (Current) Combination waveform, $R_s = 2\ \Omega$, $I_{PP} = 18A$		4.6	5.6	V
ESD Clamping Voltage ⁽³⁾	V_C	$I_{TLP} = 4A$, $t_p = 0.2/100ns$ (TLP)		3.0		V
		$I_{TLP} = 16A$, $t_p = 0.2/100ns$ (TLP)		4.4		
Dynamic Resistance ^{(3),(4)}	R_{DYN}	$t_p = 0.2/100ns$ (TLP)		0.12		Ω
Junction Capacitance	C_J	$V_R = 0V$, $f = 1MHz$		0.44	0.75	pF

Notes:

(1): ESD gun return path connected to Ground Reference Plane (GRP)

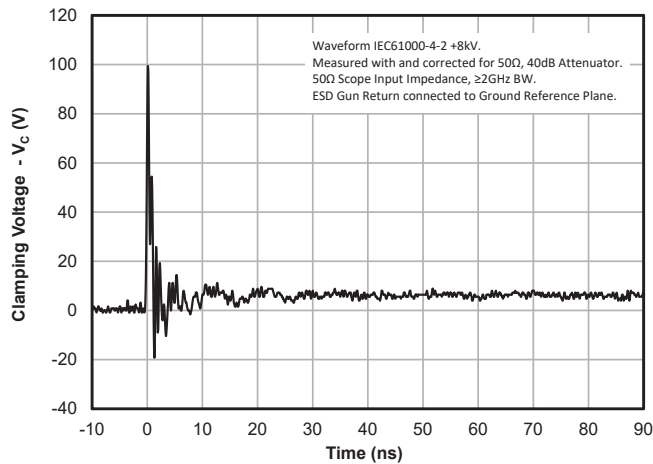
(2) Measured using a 1.2/50μs voltage, 8/20μs current combination waveform, $R_S = 2\ \Omega$. Clamping is defined as the clamping voltage after the device snaps back to a conducting state.

(3): Transmission Line Pulse Test (TLP) Settings: $t_p = 100ns$, $t_r = 0.2ns$, I_{TLP} and V_{TLP} averaging window: $t_1 = 70ns$ to $t_2 = 90ns$

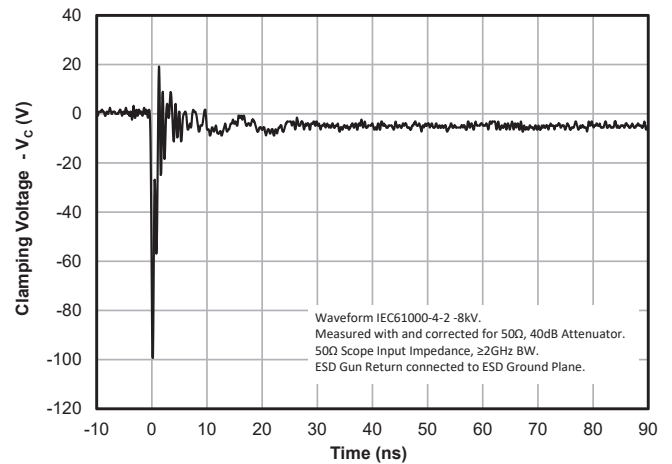
(4): Dynamic resistance calculated from $I_{TLP} = 4A$ to $I_{TLP} = 16A$

Typical Characteristics

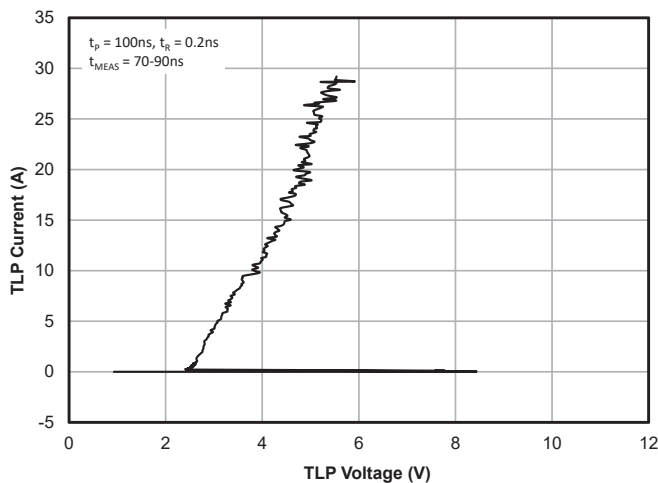
ESD Clamping (8kV Contact per IEC 61000-4-2)



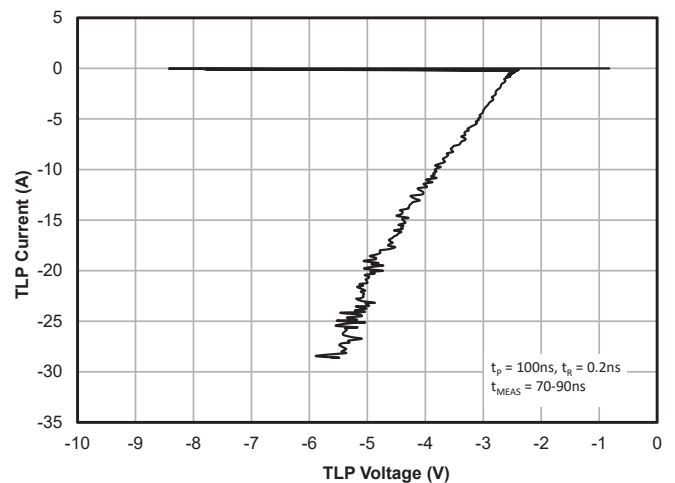
ESD Clamping (-8kV Contact per IEC 61000-4-2)



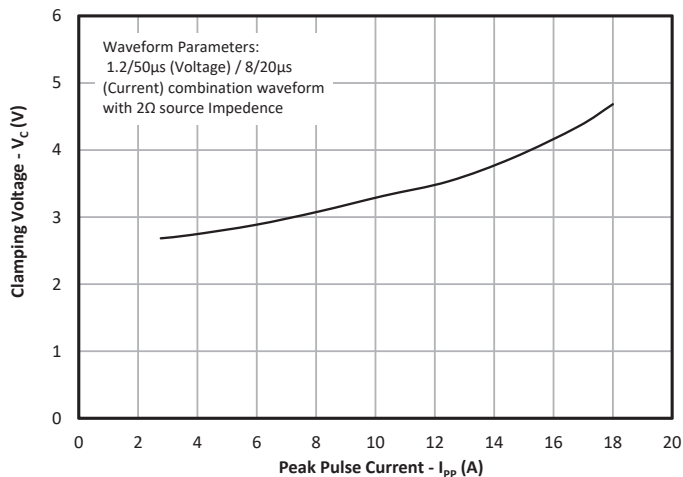
TLP Characteristics (Positive Pulse)



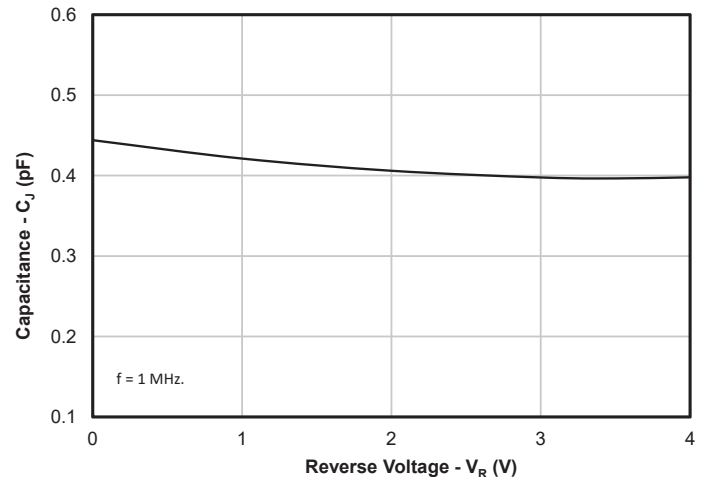
TLP Characteristics (Negative Pulse)



Clamping Voltage vs. Peak Pulse Current ($t_p = 1.2/50\mu\text{s}$)

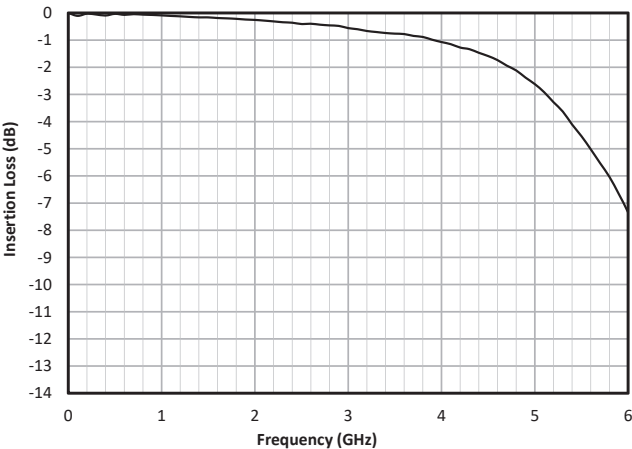


Capacitance vs. Reverse Voltage

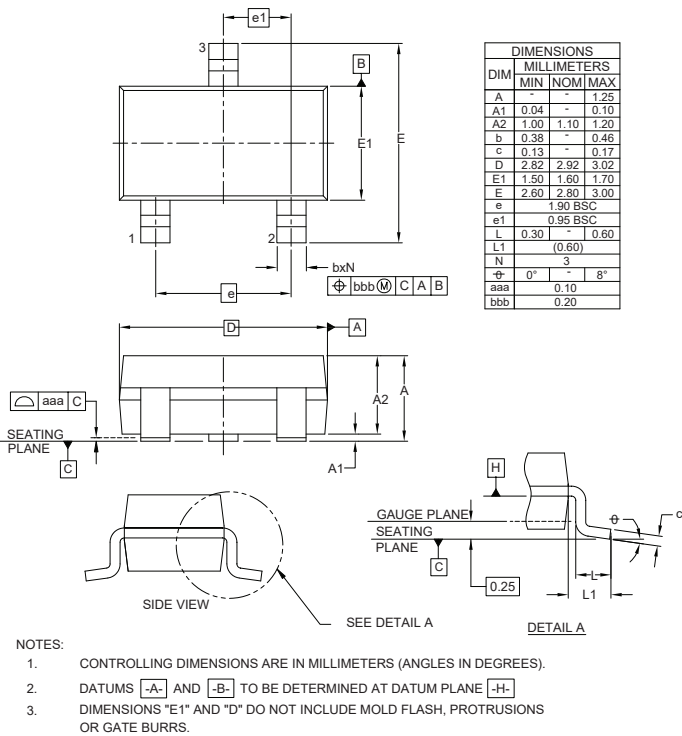


Typical Characteristics

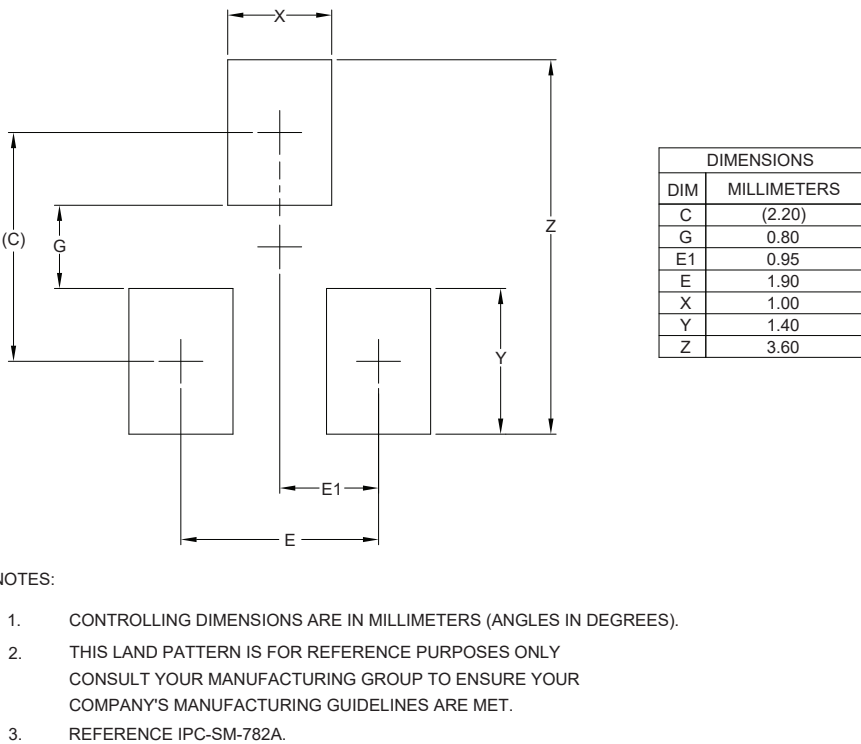
Insertion Loss - S21



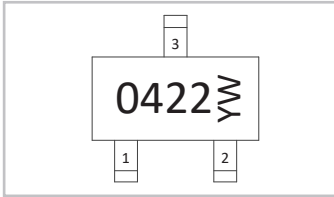
Outline Drawing - SOT23 3L



Landing Pattern - SOT23 3L

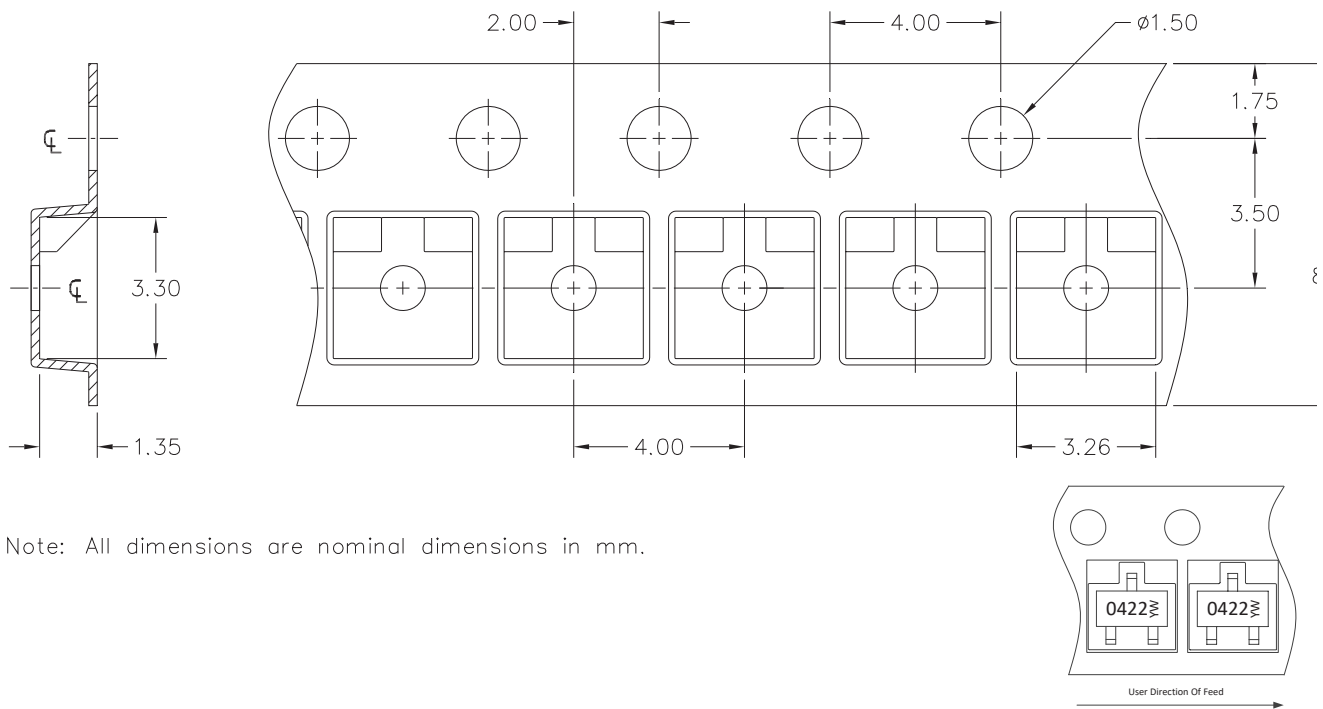


Marking Code



Note: YW = Date Code

Tape and Reel Specification



Order Information

PART NUMBER	QTY PER REEL	REEL SIZE
RClamp04022S.C	3,000	7"

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